

Robot Club Toulon : Electrical Presentation 2023

V. Barchasz, V. Gies, and al.

Université de Toulon,
Avenue de l'Université, 83130 La Garde, France
rct@univ-tln.fr
Home page : <http://rct.univ-tln.fr>

Abstract. Keywords: RoboCup Soccer, Middle-Size League, Multi-robot, Actuator Modeling

1 Electrical Presentation

Architecture of RCT robots relies on a cortex composed by an embedded computer interfacing advanced sensors such as LIDAR and cameras for positioning, scene analysis and collision avoidance, and communicating with a peripheral board for interfacing actuators and simple sensors as shown in Fig. 1. The kicking system is a third board, independent for development and safety reasons due to high voltage and EMI emissions.

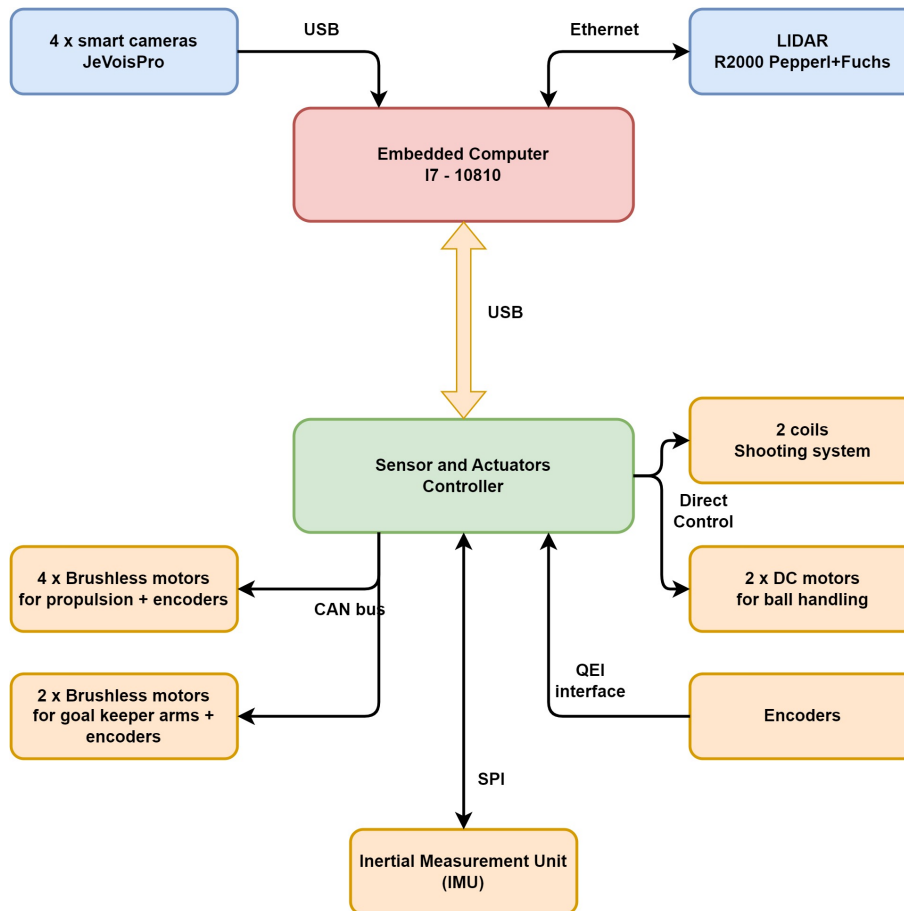


Fig. 1. RCT robots architecture description.

changer nom ordi :The embedded computer used is a *P12B-i7-10810U* programmed in C#.

Collision and obstacle avoidance is based on two types of sensors : a Pepper1+Fuchs R2000 lidar and four JeVois Pro smart cameras running under YOLOV5M.

The peripheral board is a home-designed PCB in 4-layers with a DSP™ from Microchip as main processor This board is able to drive up to six 150W motors, it manages 8 quadrature encoders and up to 20 digital or analog *I/O*. Basic sensors such as IR proximity sensors, ultra sonic telemeters, IMU and precision gyro are connected to this board using SPI, I2C or UART buses.

In 2023, an evolution on

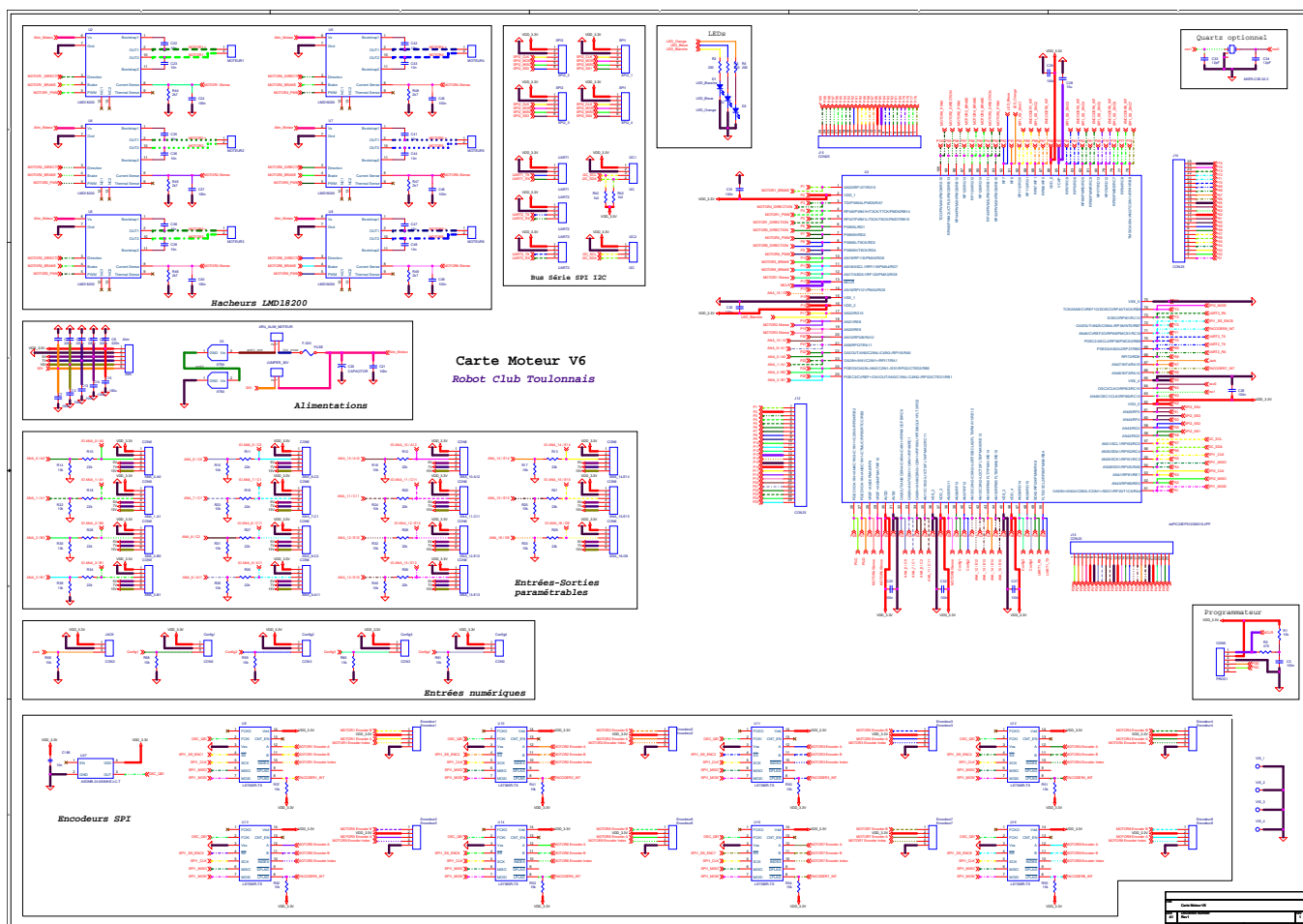


Fig. 2. Peripheral Board schematic

The power board is a secondary board used for generating different power supplies safely to the other boards. As shown on Fig. 4, 3 switched TRACO power supply having an output current up to 1A are used for generating 15V, 5V and 3.3V. 15V voltage is used by Sick industrial sensors, 5V is used by some sensors (rather old ones such GP2D12 IR sensors from Sharp, but no longer present in the robot), and 3.3V is used by the peripheral board for powering its DSP and most of the device connected to it. It is made on a 2 layers PCB

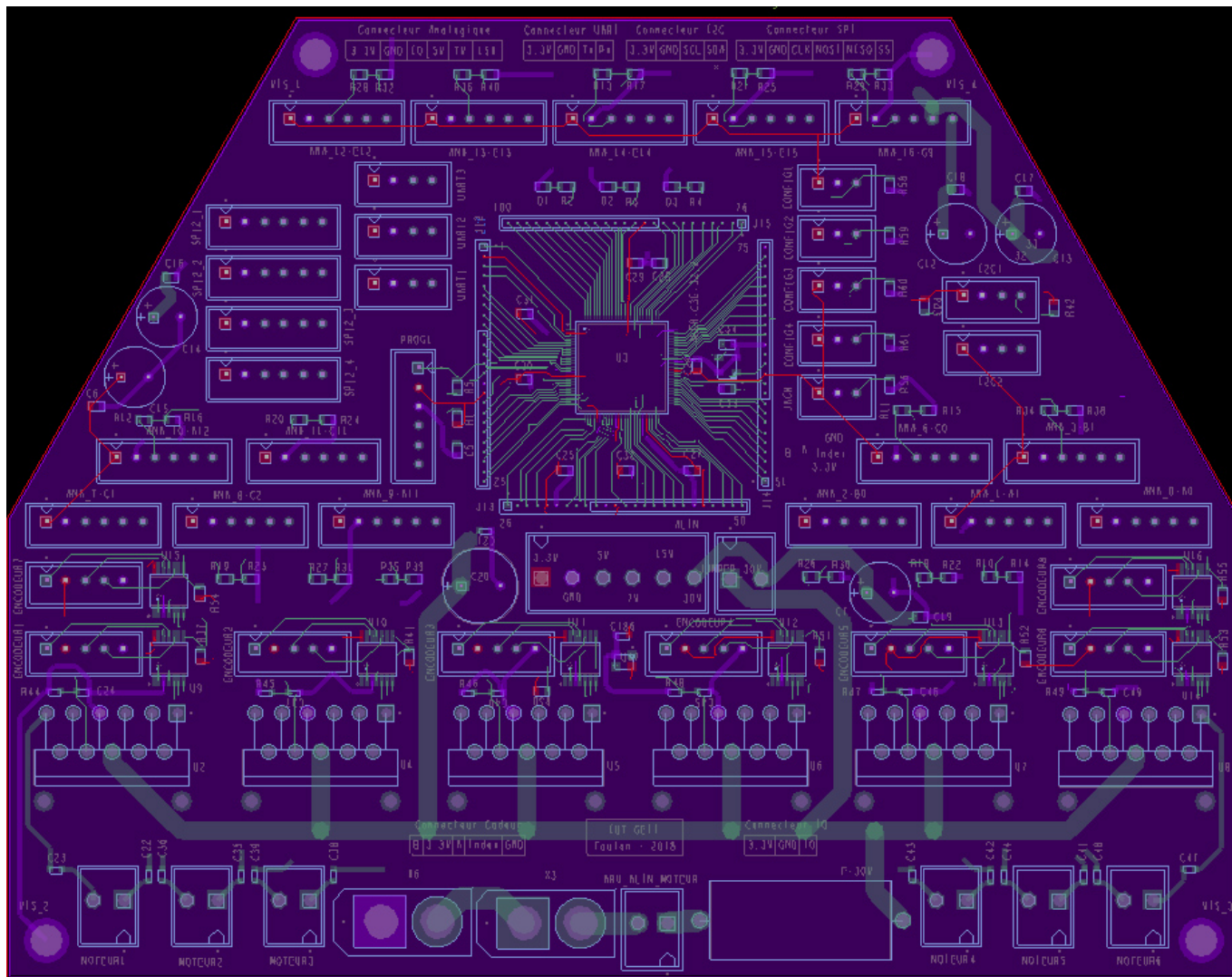


Fig. 3. Peripheral Board PCB

